

Claims

1. A method for automatically altering a magnitude of at least one component value ($C_{11} - C_{13}$; $R_{11} - R_{13}$, $C_{14} - C_{16}$) in an analogue filter (201) in an integrated circuit, the integrated circuit comprising a phase detector (204) for determining a present characteristic of the analogue filter (201) relative and desired characteristic, the method involving:

receiving a periodic reference signal (R) and a phase shifted period signal (R') in the phase detector (204) and producing a test signal (T) in response to a phase difference between the periodic reference signal (R) and the periodic phase shifted signal (R'), characterised by

the integrated circuit comprising an adjustable phase shifter (203) for receiving the periodic reference signal (R) and on basis thereof producing the period phase shifted signal (R'), the method further involving:

altering a magnitude of at least one component value ($C_{20} - C_{23}$) in the adjustable phase shifter (203) in response to a control signal (C_p), such that the phase shifted between the signal (R') attains a calibrated value which is as close as possible to a desired value, the control signal (C_p) being generated on basis of the test signal (T), and

setting the at least one component value ($C_{11} - C_{13}$; $R_{11} - R_{13}$, $C_{14} - C_{16}$) in an analogue filter (201) in according with setting of the at least one component value ($C_{20} - C_{23}$) in the adjustable phase shifter (203) which produces the calibrated value.